

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	L1	137	(717/158).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:15	
2	IS&R	L2	322	((717/158) or (717/140)).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:15	
3	IS&R	L3	230	((717/158) or (717/148)).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:15	
4	IS&R	L4	259	((717/158) or (717/149)).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:16	
5	IS&R	L5	124	(717/149).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:16	
6	IS&R	L6	96	(717/148).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:16	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	IS&R	L7	192	(717/140).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:16	
8	BRS	L8	0	1 and predicate\$4 adj register and switch\$4 and parallel	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:17	
9	BRS	L9	0	5 and predicate\$4 adj register and switch\$4 and parallel	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:17	
10	BRS	L10	0	6 and predicate\$4 adj register and switch\$4 and parallel	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:17	
11	BRS	L11	1	7 and predicate\$4 adj register and switch\$4 and parallel	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:19	
12	BRS	L12	27	predicate\$4 adj register and switch\$4 and parallel	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/05/06 13:19	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	L13	1	12 and control adj information and state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:19	
14	BRS	L14	1	12 and control adj information	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:20	
15	BRS	L15	21	12 and state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:20	
16	BRS	L16	17	15 and signals	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:20	
17	BRS	L17	0	16 and first adj state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:21	
18	BRS	L18	0	16 and second adj state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:21	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	17	16 and state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:21	
20	BRS	L21	8	20 and event	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:22	
21	BRS	L22	0	21 and shift\$4 adj operation	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:22	
22	BRS	L23	3	21 and shift\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:22	
23	BRS	L20	17	19 and processor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:31	
24	BRS	L24	66	predicat\$4 adj instruction and processor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:31	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
25	BRS	L26	1	25 and control adj information	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:32	
26	BRS	L25	26	24 and predicat\$4 adj register	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 13:33	

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6732203 B2	20040504	107	Selectively multiplexing memory coupling global bus data bits to narrower functional unit coupling local bus	710/66
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6721875 B1	20040413	31	Method and apparatus for implementing a single-syllable IP-relative branch instruction and a long IP-relative branch instruction in a processor which fetches instructions in bundle form	712/233
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6598155 B1	20030722	29	Method and apparatus for loop buffering digital signal processing instructions	712/241

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1	710/307; 712/33		Kanapathippillai, Ruban et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>					
2	712/210; 712/24		McCormick, Jr., James E et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>					
3	711/110		Ganapathy, Kumar et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>					

	Image Doc. Displayed	PT
1	US 6732203	<input type="checkbox"/>
2	US 6721875	<input type="checkbox"/>
3	US 6598155	<input type="checkbox"/>